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**In the United States Patent and Trademark Office**

**Date:** July 14, 2006

**In re Application of:** Watanabe et al.

**Filed:** 05/07/2004

**For:** Printed Circuit Board Manufacturing Method

**Serial Number:** 10/709,474

**Art Unit:** 1753

**Examiner:** Luan V. Van

**Amendment Under 37 C.F.R. §1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed on April 26, 2006, which is due for response by July 26, 2006. No fee is due by virtue of the present response. If the USPTO determines that a fee is due, please charge Applicants' deposit account, 09-0456, for any fee(s) that the USPTO determines is due.

Applicants request that the claims be amended as follows. No new matter has been included.